DiMES Slot Geometry Analysis

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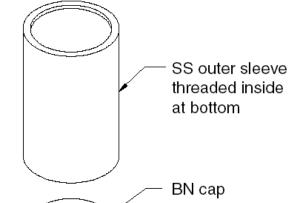
ALPS/PFC Meeting UIUC, May 4th, 2004

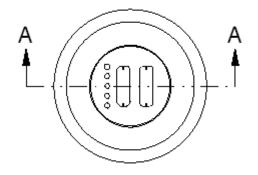


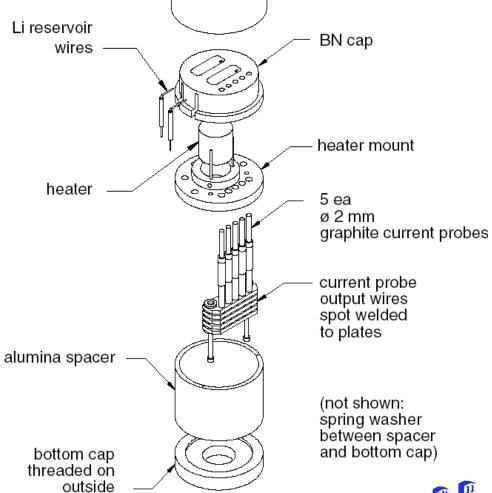


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DIMES # 121 version D

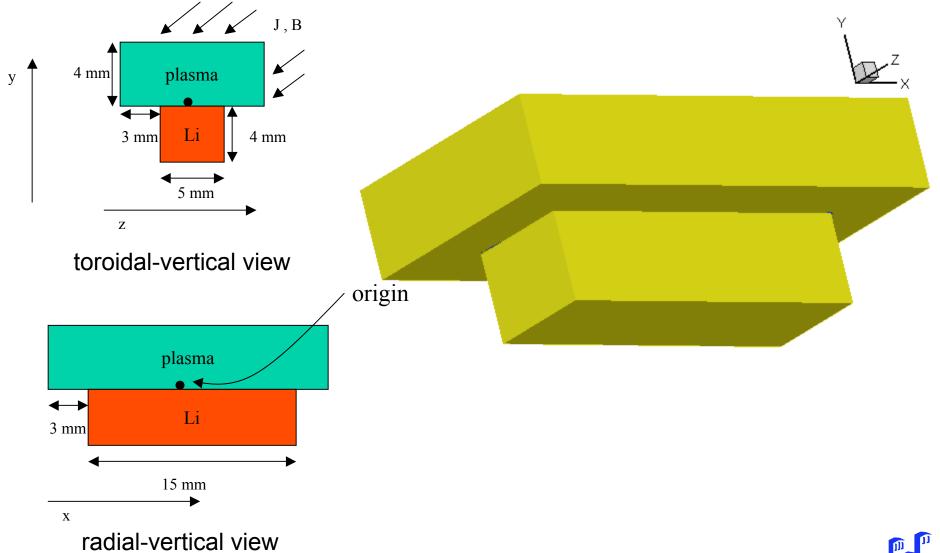






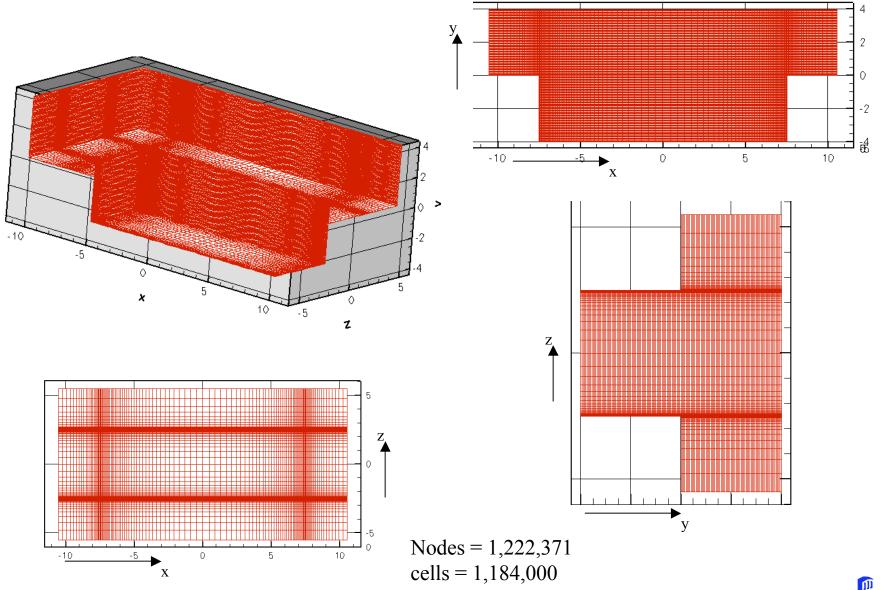


DiMES slot geometry



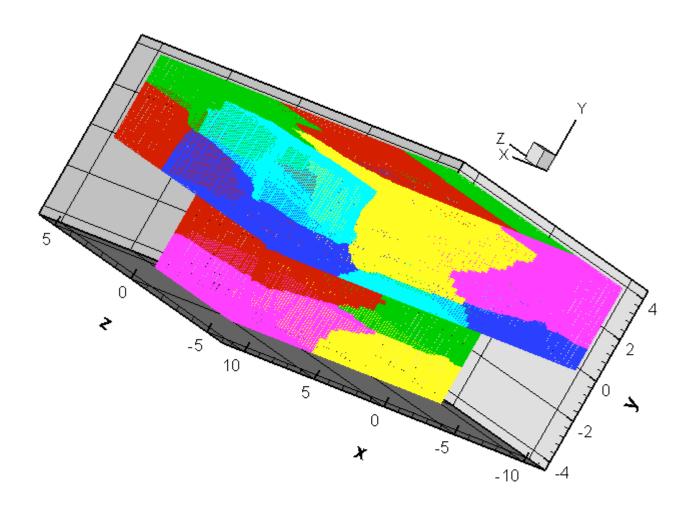


Block Mesh – Hartmann Layers Resolved



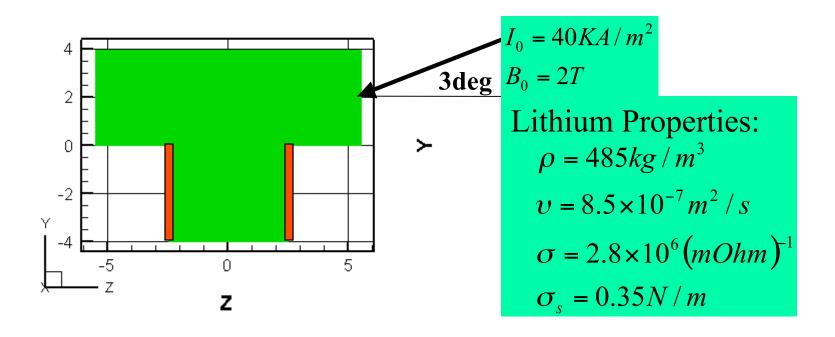


Mesh and its partition to 16 processors





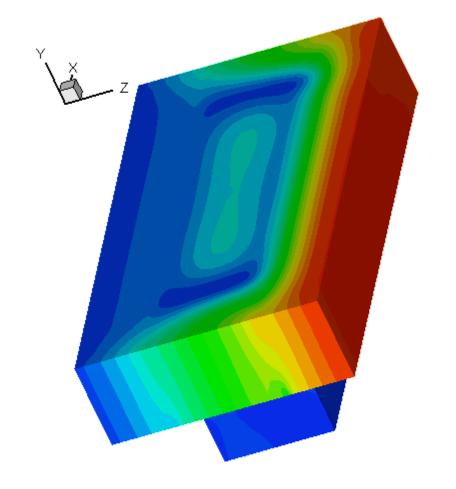
Case I: Grounding side of slot

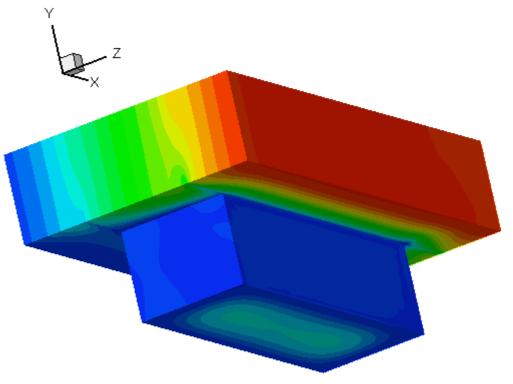


- Top and bottom boundaries: insulated wall with dφ/dn=0
- Right top part: applied current boundary condition
- All other boundaries are ground with $\phi = 0$



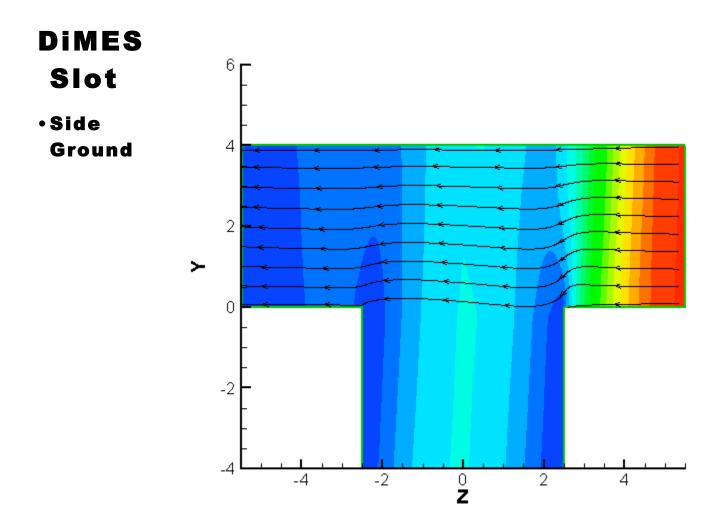
·Side Ground





Electrical potential at t=15ms

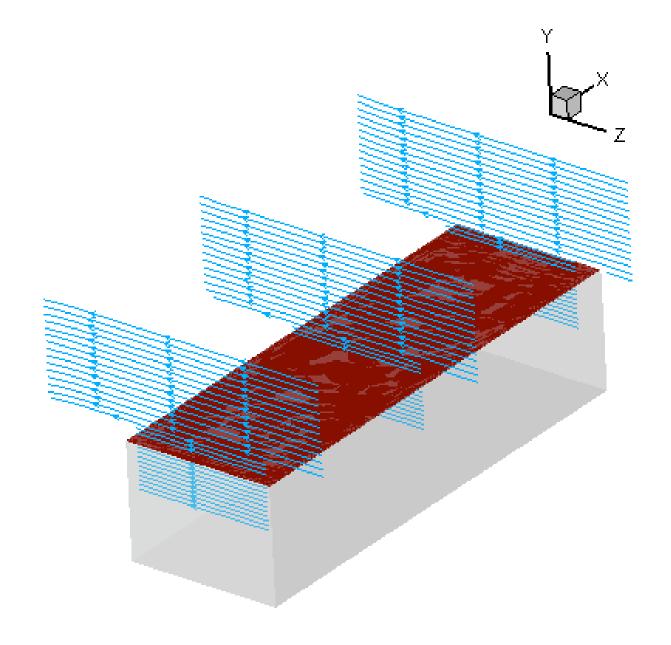




Electrical potential contour and current distribution on the cross section of x=0



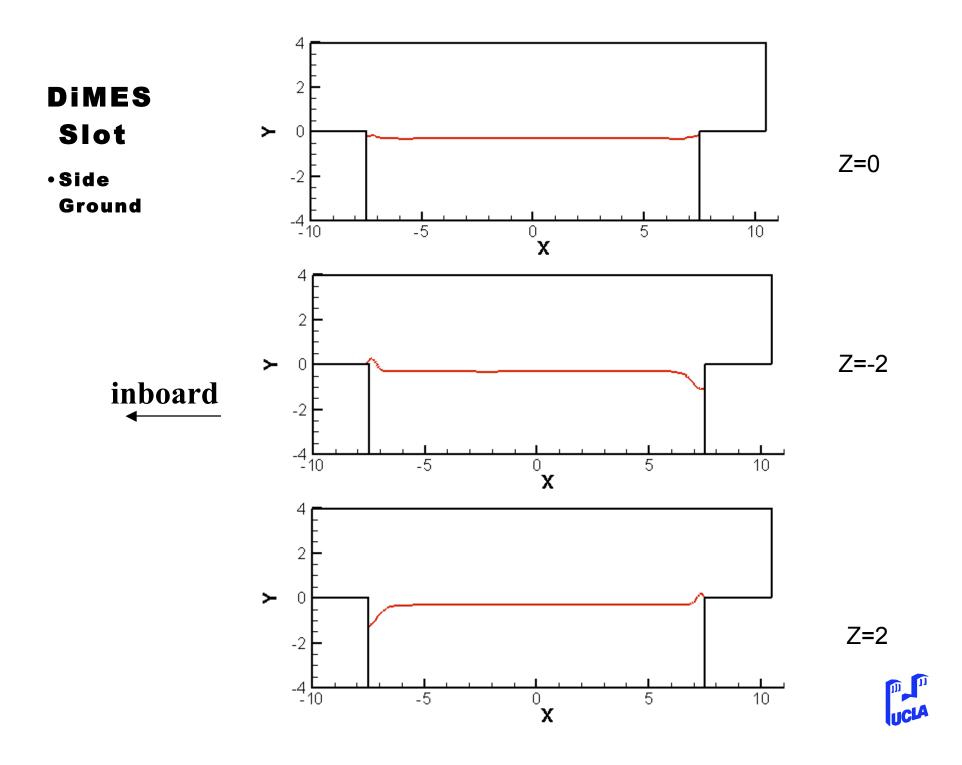
- ·Side Ground
- •0-12 ms



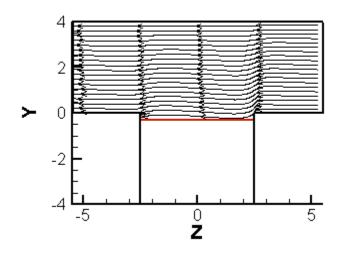


- ·Side Ground
- •0-12 ms



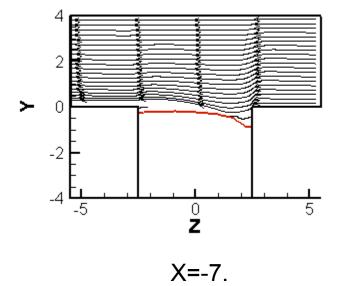


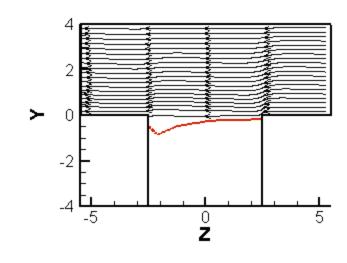
·Side Ground



X=0.

Toroidal

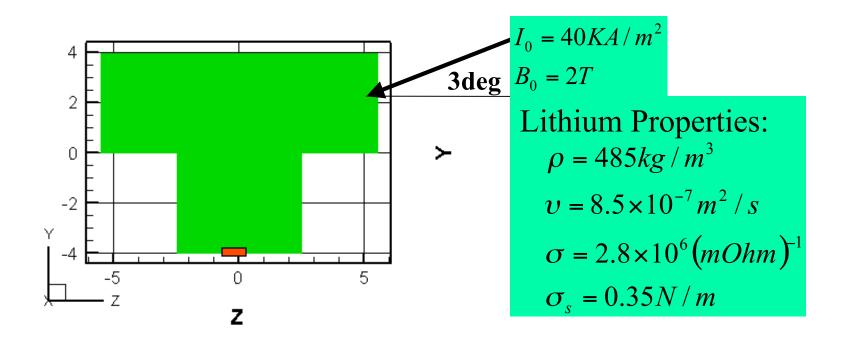




X=7.



Case II with Bottom Grounding



- Right top part: applied current boundary condition
- There is a ground patch located at the central of bottom wall parallel to x axis.
- Left top part: zero electrical potential boundary condition ϕ = 0, and all other parts are insulated wall with $d\phi/dn$ = 0

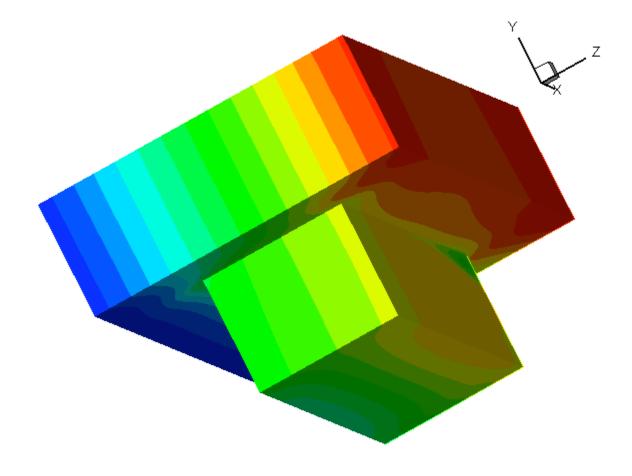


DIMES Slot Bottom Ground

Interface shape at t=21ms



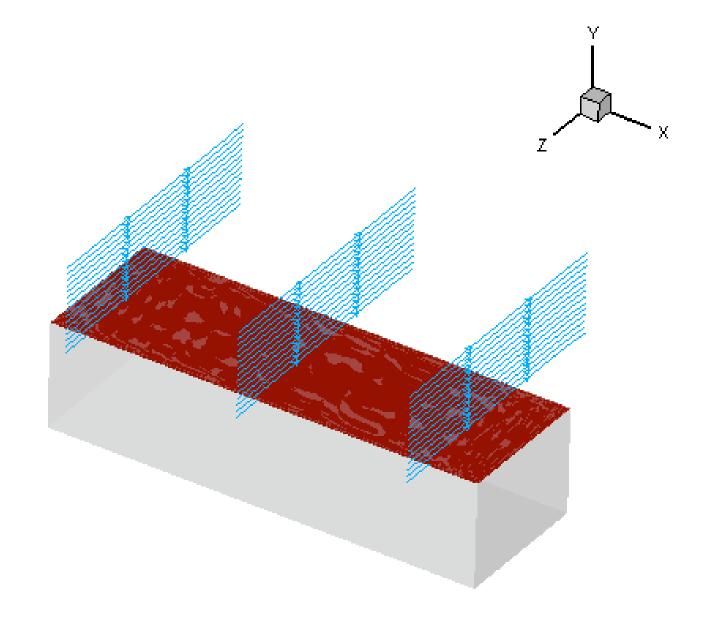
BottomGround



Electrical potential at t=21ms



- BottomGround
- •0-21 ms





- BottomGround
- •0-21 ms





BottomGround

inboard

